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Design of low-power and low-phase noise VCO in standard 0 ...

Abstract: A voltage-controlled oscillator (VCO) with low phase noise and low power dissipation for sub-harmonic mixing is proposed. A cross-coupled negative-resistance circuit with nMOS transistors in the feedback is adopted to suppress high-order harmonics.

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The performance of voltage controlled oscillators (VCO) is of extreme importance for any telecommunication or data communication system. This practical guide develops a systematic, fully-integrated LC-VCO design for low power and low phase noise, especially useful to meet the demands on mobile devices such as cell phones.

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Novel LC Tank Steers Low-Power VCO - mwrwf.com

This VCO is designed with an LC tank circuit and switched-capacitor techniques to overcome the usual tradeoffs in output power, phase noise, and tuning range at low operating voltages.

A Low Power Voltage Controlled Oscillator Design

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s Editorial Board as well as its Table of Contents are divided into 108 subject areas that are covered within the journal's scope.

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[A Low Power 3-Stage Voltage-Controlled Ring Oscillator in ...](#)

of this research work is to design a low power VCO at 2.45 GHz in the ISM band. In addition, in order to reduce overall oscillator size, ring based architecture has been adopted with easy integration technique. For varying the oscillating frequency from 2.2 GHz to 2.85 GHz, PMOS transistors with fixed value capacitors are utilized.

Providing 1.8 V supply, the oscillator dissipates 6.99 mW of

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The performance of voltage controlled oscillators (VCO) is of extreme importance for any telecommunication or data communication system. This practical guide develops a systematic, fully-integrated LC-VCO design for low power and low phase noise, especially useful to meet the demands on mobile devices such as cell phones.

[Low noise and low power voltage-controlled oscillator \(VCO\) ...](#)

Low noise and low power voltage-controlled oscillator (VCO) using transconductance (gm) degeneration Mar 11, 2014 - QUALCOMM INCORPORATED Certain aspects of the present disclosure generally relate to voltage-controlled oscillators (VCOs) using a lowered or an adjustable negative transconductance (-gm) compared to conventional VCOs.

[A low-power 2.4-GHz combined LNA VCO structure in 0.13- CMOS](#)

3 Low-power LC-VCO design A wide range of oscillator topologies is available in mod-ern integrated circuits including ring oscillators, relaxation oscillators, and LC oscillators. Given a power budget, LC oscillators typically exhibit a lower phase noise due to the high energy-storage capability of their LC tank [13]. Thus, they are usually the preferred choice for wireless commu-nication

[\(PDF\) Design method for an ultra low power, low offset ...](#)

A design method for ultra low power, low offset,

symmetric OTAs is presented. The method is based on the gm/ID methodology and uses a model of MOS transistor valid in all the regions of operation.

A New Low-Power CMOS Quadrature VCO with Current Reused ...

360 C. WANG, G. PENG, M. MA, Z. LI. A LOW-POWER CMOS QUADRATURE VCO WITH CURRENT REUSED STRUCTUR. A New Low-Power CMOS Quadrature VCO with Current Reused Structure